



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET

Bib Data Sheet

CONFIRMATION NO. 8529

| | | | | |
|--|---|------------------------|---|------------------------------------|
| SERIAL NUMBER 10/600,393 | FILING DATE 06/20/2003 RULE | CLASS 438 | GROUP ART UNIT 2822 | ATTORNEY DOCKET NO. TS02-066 |
| APPLICANTS Chi-Chun Chen, Kaohsiung, TAIWAN; Tzu-Liang Lee, Hsinchu, TAIWAN; Shih-Chang Chen, Hsin-Chu, TAIWAN; ** CONTINUING DATA ***** JmJ None ** FOREIGN APPLICATIONS ***** JmJ None IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/25/2004 | | | | |
| Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Met after Allowance met Verified and Acknowledged <i>Jonice M. Thomas</i> JmJ Examiner's Signature Initials | STATE OR COUNTRY TAIWAN | SHEETS DRAWING 2 | TOTAL CLAIMS 33 | INDEPENDENT CLAIMS 2 |
| ADDRESS GEORGE O. SAILE 28 DAVIS AVENUE POUGHKEEPSIE, NY 12603 | | | | |
| TITLE Method of forming dual gate insulator layers for CMOS applications | | | | |
| FILING FEE RECEIVED 984 | FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following: | | <input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit | |